

## Recommendations from the digital group to proceed further

- Test pulser at input of analog board
- Power supply  $\pm 5V$ , 3.3V and 2.5V only and by switched power supplies also for analog frontend
- ADC-input stage AC 2V to 3 V ?
- Current monitor **and** fluctuation analysis both including engineering array - **decision after engineering array**
- Virtual channel
- Diminishing of undershoot by using receiver instead of transformer
- Integration time as short as possible for better reconstruction (**factor 2 shorter than now?**)